Features & Benefits

- RoHS compliant (with F or G pin style)
- EMI filtering
- Choice of 500W or 750W modules
- 96% efficiency
- Autoranging 115/230\text{V}_{\text{AC}} input
- Microprocessor controlled
- Inrush current limiting
- Mini sized package
- Power fail signal
- Module enable

Product Highlights

The FARM (Filter/Autoranging Rectifier Module) is an AC front-end module which provides EMI filtering, autoranging line rectification and inrush current limiting. The FARM is available in either 500/750W or 750/1000W models in a mini sized package measuring only 2.28” x 2.2” x 0.5”.

The FARM interfaces directly with worldwide AC mains and may be used with Vicor 300V input DC-DC converters to realize an autoranging, high density, low profile switching power supply. The FARM includes a microcontroller that continuously monitors the AC line to control bridge / doubler operation. The user need only provide external capacitance to satisfy system hold-up requirements.

Vicor 2nd Generation packaging technology offers flexible mounting options for various manufacturing processes. The FARM may be installed as a conventional leaded device for onboard applications, inboard for low profile, height restricted applications, socketed or surface mounted with optional ModuMate interconnect products.

Absolute Maximum Rating

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Rating</th>
<th>Unit</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>L to N voltage</td>
<td>264</td>
<td>\text{V}_{\text{AC}}</td>
<td>Continuous</td>
</tr>
<tr>
<td>+OUT to –OUT voltage</td>
<td>400</td>
<td>\text{V}_{\text{DC}}</td>
<td>100 ms</td>
</tr>
<tr>
<td>B OK to –OUT voltage</td>
<td>16</td>
<td>\text{V}_{\text{DC}}</td>
<td></td>
</tr>
<tr>
<td>EN to –OUT voltage</td>
<td>16</td>
<td>\text{V}_{\text{DC}}</td>
<td></td>
</tr>
<tr>
<td>Mounting torque</td>
<td>4 – 6 (0.45 – 0.68)</td>
<td>in-lbs (N-m)</td>
<td>6 each, 4-40 screw</td>
</tr>
<tr>
<td>Operating temperature</td>
<td>–40 to 100</td>
<td>°C</td>
<td>H-Grade</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>– 55 to 125</td>
<td>°C</td>
<td>H-Grade</td>
</tr>
<tr>
<td>Pin soldering temperature</td>
<td>500 (260)</td>
<td>°F (°C)</td>
<td>&lt;5 sec; wave solder</td>
</tr>
<tr>
<td></td>
<td>750 (390)</td>
<td>°F (°C)</td>
<td>&lt;7 sec; hand solder</td>
</tr>
<tr>
<td>Output current</td>
<td>3.5</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>Baseplate temperature</td>
<td>100</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

Thermal Resistance and Capacity

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Baseplate to sink</td>
<td>0.16</td>
<td></td>
<td></td>
<td>°C/Watt</td>
</tr>
<tr>
<td>Flat, greased surface</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>with thermal pad (PN 20264)</td>
<td>0.14</td>
<td></td>
<td></td>
<td>°C/Watt</td>
</tr>
<tr>
<td>Baseplate to ambient</td>
<td>8.0</td>
<td></td>
<td>1.9</td>
<td>°C/Watt</td>
</tr>
<tr>
<td>Free convection</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1000LFM</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Part Numbering

<table>
<thead>
<tr>
<th>Product</th>
<th>Type</th>
<th>C</th>
<th>1</th>
<th>1</th>
<th>1</th>
</tr>
</thead>
<tbody>
<tr>
<td>FARM</td>
<td>1 = 500/750W</td>
<td>2 = 750/1000W</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Product Grade Temperatures (°C)

- \text{E} = –10 to +100
- \text{C} = –20 to +100
- \text{T} = –50 to +100

Pin Style

- 1 = Short Pin
- 2 = Long Pin
- \text{S} = Short ModuMate \[^1\]
- \text{N} = Long ModuMate \[^1\]
- \text{F} = Short RoHS \[^1\]
- \text{G} = Long RoHS \[^1\]
- \text{K} = Extra Long RoHS \[^1\]

Baseplate

- 1 = Slotted
- 2 = Threaded
- 3 = Thru hole

\[^1\] Pin styles S, N, F & G are compatible with the ModuMate interconnect system for socketing and surface mounting

\[^2\] Not intended for socket or SurfMate mounting
**Electrical Characteristics**

Electrical characteristics apply over the full operating range of input voltage, output power and baseplate temperature, unless otherwise specified. All temperatures refer to the operating temperature at the center of the baseplate. Specifications apply for AC mains having up to 5% total harmonic distortion.

**INPUT SPECIFICATIONS (FARM1xxx, FARM2xxx)**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating input voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>low range</td>
<td>90</td>
<td>132</td>
<td>V&lt;sub&gt;AC&lt;/sub&gt;</td>
<td>Autoranging (doubler mode)</td>
<td></td>
</tr>
<tr>
<td>high range</td>
<td>180</td>
<td>264</td>
<td>V&lt;sub&gt;AC&lt;/sub&gt;</td>
<td>Autoranging (bridge mode)</td>
<td></td>
</tr>
<tr>
<td>Input undervoltage</td>
<td>90</td>
<td></td>
<td>V&lt;sub&gt;AC&lt;/sub&gt;</td>
<td>No damage</td>
<td></td>
</tr>
<tr>
<td>AC line frequency</td>
<td>47</td>
<td>63</td>
<td>Hz</td>
<td>C-Grade</td>
<td></td>
</tr>
<tr>
<td></td>
<td>47</td>
<td>880</td>
<td>Hz</td>
<td>T-Grade, H-Grade</td>
<td></td>
</tr>
<tr>
<td>Power factor</td>
<td>0</td>
<td>0.6</td>
<td>%</td>
<td>Dependent on line source impedance, hold up capacitance and load.</td>
<td></td>
</tr>
<tr>
<td>Inrush current</td>
<td></td>
<td>30</td>
<td>Amps</td>
<td>264 V&lt;sub&gt;AC&lt;/sub&gt; peak line</td>
<td></td>
</tr>
</tbody>
</table>

**OUTPUT SPECIFICATIONS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>FARM1xxx</th>
<th>FARM2xxx</th>
<th></th>
<th></th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output power</td>
<td>0 – 500</td>
<td>0 – 750</td>
<td>0 – 750</td>
<td>Watts</td>
<td>90 – 132 V&lt;sub&gt;AC&lt;/sub&gt;</td>
</tr>
<tr>
<td>Efficiency</td>
<td>94 – 96</td>
<td>%</td>
<td>94 – 96</td>
<td>%</td>
<td>120/240 V&lt;sub&gt;AC&lt;/sub&gt;</td>
</tr>
<tr>
<td>Output voltage</td>
<td>250 – 370</td>
<td>250 – 370</td>
<td>250 – 370</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>90 – 264 V&lt;sub&gt;AC&lt;/sub&gt;</td>
</tr>
<tr>
<td>External hold-up capacitance</td>
<td>1,750 µF</td>
<td>2 – 3,300 µF in series; HUB3300-S</td>
<td>1,100 µF</td>
<td>2 – 2,200 µF in series; HUB2200-S</td>
<td></td>
</tr>
</tbody>
</table>

**CONTROL PIN SPECIFICATIONS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC Bus OK (B OK)</td>
<td>15</td>
<td></td>
<td></td>
<td>Ω</td>
<td>To negative output – Bus normal</td>
</tr>
<tr>
<td>Low state resistance</td>
<td>0</td>
<td>0.1</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Bus normal 50mA max.</td>
<td></td>
</tr>
<tr>
<td>High state voltage</td>
<td>14.0</td>
<td>15.0</td>
<td>15.4</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Bus abnormal, 27kΩ internal pull up to 15V&lt;sub&gt;DC&lt;/sub&gt; (see Figure 11)</td>
</tr>
<tr>
<td>B OK true threshold</td>
<td>235</td>
<td>240</td>
<td>245</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Output bus voltage (see Figure 8)</td>
</tr>
<tr>
<td>B OK false threshold</td>
<td>200</td>
<td>205</td>
<td>210</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Output bus voltage</td>
</tr>
<tr>
<td>Module Enable (EN)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Low state resistance</td>
<td>15</td>
<td></td>
<td></td>
<td>Ω</td>
<td>To negative output – Converters disabled</td>
</tr>
<tr>
<td>Low state voltage</td>
<td>0.1</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>50mA max.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>High state voltage</td>
<td>12</td>
<td>14</td>
<td>16</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>150kΩ internal pull up to 15V&lt;sub&gt;DC&lt;/sub&gt; (see Figure 10)</td>
</tr>
<tr>
<td>Enable threshold</td>
<td>235</td>
<td>240</td>
<td>245</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Output bus voltage (see Figure 8)</td>
</tr>
<tr>
<td>Disable threshold</td>
<td>185</td>
<td>190</td>
<td>195</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Output bus voltage</td>
</tr>
<tr>
<td>AC Bus OK - Module Enable, differential error*</td>
<td>12</td>
<td>14</td>
<td>16</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>AC Bus OK and Module Enable thresholds track</td>
</tr>
</tbody>
</table>

* Tracking error between BUS OK and Enable thresholds
### Electrical Characteristics (Cont.)

#### ELECTROMAGNETIC COMPATIBILITY

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Standard</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Transient / surge immunity</td>
<td>EN61000-4-5</td>
<td>2kV–50µs Line or neutral to earth</td>
</tr>
<tr>
<td>Line disturbance / immunity</td>
<td>EN61000-4-11</td>
<td>Intermittent and brownouts</td>
</tr>
<tr>
<td>Flicker / inrush</td>
<td>EN61000-3-3</td>
<td></td>
</tr>
</tbody>
</table>

#### SAFETY SPECIFICATIONS (FARM1xxx, FARM2xxx)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Isolation voltage (IN to OUT)</td>
<td>None</td>
<td></td>
<td></td>
<td></td>
<td>Isolation provided by DC-DC converter(s)</td>
</tr>
<tr>
<td>Dielectric withstand (I/O to baseplate)</td>
<td>2,121</td>
<td>V&lt;sub&gt;DC&lt;/sub&gt;</td>
<td>Baseplate earthed</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Leakage current</td>
<td>1.5 mA</td>
<td></td>
<td>264 V&lt;sub&gt;AC&lt;/sub&gt;</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### AGENCY APPROVALS

<table>
<thead>
<tr>
<th>Safety Standards</th>
<th>Agency Markings</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>FARM1 xxx</td>
<td>cTÜVus</td>
<td>Baseplate earthed, fast acting line fuse,</td>
</tr>
<tr>
<td>UL60950, EN60950, CSA 60950</td>
<td></td>
<td>Bussman ABC10 or Wickman 10A 194 series</td>
</tr>
<tr>
<td>CE Marked</td>
<td></td>
<td>Low voltage directive</td>
</tr>
<tr>
<td>FARM2 xxx</td>
<td>cTÜVus</td>
<td>Baseplate earthed, fast acting line fuse,</td>
</tr>
<tr>
<td>UL60950, EN60950, CSA 60950</td>
<td></td>
<td>Bussman ABC15 or Wickman 16A 194 Series</td>
</tr>
<tr>
<td>CE Marked</td>
<td></td>
<td>Low voltage directive</td>
</tr>
</tbody>
</table>

#### GENERAL SPECIFICATIONS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>MTBF</td>
<td>&gt;1,000,000</td>
<td>Hours</td>
<td></td>
<td></td>
<td>25°C, Ground Benign, MIL-HDBK-217F</td>
</tr>
<tr>
<td>Baseplate material</td>
<td></td>
<td>Aluminum</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Cover</td>
<td></td>
<td>Ends: Zenite® 6130</td>
<td>Center section: Kapton insulated aluminum</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Pin Material</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Style 1 &amp; 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Copper, Tin / Lead plated</td>
</tr>
<tr>
<td>Styles S &amp; N (ModuMate compatible)</td>
<td></td>
<td></td>
<td>Copper, Nickel / Gold plated</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Styles F &amp; G (RoHS compliant)</td>
<td></td>
<td></td>
<td>Copper, Nickel / Gold plated</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Weight</td>
<td>3.1 (88)</td>
<td>Ounces</td>
<td></td>
<td>264 V&lt;sub&gt;AC&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>Size</td>
<td>2.28 x 2.2 x 0.5 (57.9 x 55.9 x 12.7)</td>
<td>Inches</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Operating Characteristics

**Figure 1** — Start up at 120V\(_{AC}\) input

**Figure 2** — Start up at 240V\(_{AC}\) input

**Figure 3** — Power down, from 120V\(_{AC}\)

**Figure 4** — Power down, from 240V\(_{AC}\)

**Figure 5** — Output overvoltage protection 240V\(_{AC}\) range

**Figure 6** — Typical Conducted Emissions
Application Note

The Filtered, Autoranging Rectifier Module (FARM) provides an effective solution for the AC frontend of a power supply built with Vicor DC-DC converters. This high-performance power system building block satisfies a broad spectrum of requirements and agency standards.

In addition to providing transient/surge immunity and EMI filtering, the FARM contains all of the power switching and control circuitry necessary for autoranging rectification, inrush current limiting, and overvoltage protection. This module also provides converter enable and status functions for orderly power up/down control or sequencing. To complete the AC front-end configuration, the user only needs to add hold-up capacitors, and a few discrete components.

Functional Description

Initial Conditions: The switch that bypasses the inrush limiting PTC (positive temperature coefficient) thermistor is open when power is applied, as is the switch that engages the strap for voltage doubling. (See Figure 7). In addition, the converter modules are disabled via the Enable (EN) line, and BusOK (BOK) is high.

Power Up Sequence (See Figure 8):

1. Upon application of input power, the output bus capacitors begin to charge. The thermistor limits the charge current, and the exponential time constant is determined by the hold-up capacitor value and the thermistor cold resistance. The slope (dV/dt) of the capacitor voltage versus time approaches zero as the capacitors become charged to the peak of the AC line voltage.

2. If the bus voltage is greater than 200V, the doubler is not activated.

3. If the bus voltage is greater than 235V as the slope approaches zero, the inrush limiting thermistor is bypassed. Below 235V, it is not bypassed.

4. The converters are enabled ~150 milliseconds after the thermistor bypass switch is closed.

5. Bus-OK is asserted after an additional ~150 millisecond delay to allow the converter outputs to settle within specification.

Power Down Sequence: (See Figure 8). When input power is turned off or fails, the following sequence occurs as the bus voltage decays:

1. Bus-OK is de-asserted when the bus voltage falls below 205V_{DC} (Typical).

2. The converters are disabled when the bus voltage falls below 190V_{DC}. If power is reapplied after the converters are disabled, the entire power-up sequence is repeated. If a momentary power interruption occurs and power is re-established before the bus reaches the disable threshold, the power up sequence is not repeated, i.e., the power conversion system “rides through” the momentary interruption.

Figure 7 — Functional block diagram: autoranging rectifier

Figure 8 — Timing diagram: power up/down sequence
Application Note (Cont.)

Off-Line Power Supply Configuration

The FARM maintains the DC output bus voltage between 250 and 370V<sub>DC</sub> over the entire input voltage range, which is compatible with Vicor 300V input converters. The FARM automatically switches to the proper bridge or doubler mode depending on the input voltage, eliminating the possibility of damage due to improper line connection. The FARM1xxx is rated at 500W in the low range (90 – 132V<sub>AC</sub> input), and 750W in the high range (180 – 264V<sub>AC</sub> input). The FARM2xxx is rated for 750W and 1000W for the low and high input ranges respectively. Either of these modules can serve as the AC front end for any number and combination of compatible converters as long as the maximum power rating is not exceeded.

**Strap (ST) Pin:** In addition to input and output power pin connections, it is necessary to connect the Strap pin to the center junction of the series hold-up capacitors (C1, C2, see Figure 9) for proper (autoranging) operation. Metal oxide varistors, V1 and V2 provide capacitor protection. The bleeder resistors (R1, R2, see Figure 9) discharge the hold-up capacitors when power is switched off. Capacitors C7 and C8 are recommended if the hold-up capacitors are located more than 3 inches (75mm) from the FARM output pins.

**Enable (EN) Pin:** (See Figure 10) The Enable pin must be connected to the PC or Gate-In pin of all converter modules to disable the converters during power up. Otherwise, the converters would attempt to start while the hold-up capacitors were being charged through an unbypassed current limiting thermistor, preventing the bus voltage from reaching the thermistor bypass threshold, thus disabling the power supply. The Enable output (the drain of an N channel MOSFET) is internally pulled up to 15V through a 150kΩ resistor.

A signal diode should be placed close to and in series with the PC or (Gate-In) pin of each converter to eliminate the possibility of control interference between converters. The Enable pin switches to the high state (15V) with respect to the negative output power pin to turn on the converters after the power up inrush is over. The Enable function also provides input overvoltage protection for the converters by turning off the converters if the DC bus voltage exceeds 400V<sub>DC</sub>. The thermistor bypass switch opens if this condition occurs, placing the thermistor in series with the input voltage, which reduces the bus voltage to a safe level while limiting input current in case the varistors conduct. The thermistor bypass switch also opens if a fault or overload reduces the bus voltage to less than 180V<sub>DC</sub>.

**CAUTION:** There is no input to output isolation in the FARM, hence the –OUT of the FARM and thus the –In of the downstream DC-DC converter(s) are at a high potential. If it is necessary to provide an external enable/ disable function by controlling the DC-DC converter’s PC pin (referenced to the –IN) of the converter an opto-isolator or isolated relay should be employed.

---

**Figure 9 — Offline Power Supply Configuration**
Application Note (Cont.)

Figure 10 — Enable (EN) function

Bus-OK (BOK) Pin: (See Figure 11) The Bus-OK pin is intended to provide early-warning power fail information and is also referenced to the negative output pin.

CAUTION: There is no input-to-output isolation in the FARM. It is necessary to monitor Bus OK via an optoisolator if it is to be used on the secondary (output) side of the converters. A line-isolation transformer should be used when performing scope measurements. Scope probes should never be applied simultaneously to the input and output as this will damage the module.

Filter: (See Figure 12) An integral input filter consists of a common mode choke and Y rated capacitors (line-ground) plus two X rated capacitors (line-line). This filter configuration provides common mode and differential mode insertion loss in the frequency range between 100kHz and 30MHz as illustrated in Figure 6.

Hold-up Capacitors: Hold-up capacitor values should be determined according to output bus voltage ripple, power fail hold-up time, and ride-through time (See Figure 13). Many applications require the power supply to maintain output regulation during a momentary power failure of specified duration, i.e., the converters must hold-up or ride-through such an event while maintaining undisturbed output voltage regulation. Similarly, many of these same systems require notification of an impending power failure in order to allow time to perform an orderly shut down.

The energy stored on a capacitor which has been charged to voltage $V$ is:

$$
\varepsilon = \frac{1}{2}(C V^2)
$$

Where:  
$\varepsilon =$ stored energy  
$C =$ capacitance  
$V =$ voltage across the capacitor

Energy is given up by the capacitors as they are discharged by the converters. The energy expended (the power-time product) is:

$$
\varepsilon = P \Delta t = C (V_1^2 - V_2^2) / 2
$$

Where:  
$P =$ operating power  
$\Delta t =$ discharge interval  
$V_1 =$ capacitor voltage at the beginning of $\Delta t$  
$V_2 =$ capacitor voltage at the end of $\Delta t$

Rearranging Equation 2 to solve for the required capacitance:

$$
C = \frac{2P \Delta t}{(V_1^2 - V_2^2)}
$$
Application Note (Cont.)

The power fail warning time ($\Delta t$) is defined as the interval between B OK and converter shut down (EN) as illustrated in Fig. 13. The Bus-OK and Enable thresholds are 205V and 190V, respectively. A simplified relationship between power fail warning time, operating power, and bus capacitance is obtained by inserting these constants:

$$C = \frac{2P\Delta t}{(205^2 - 190^2)}$$  

$$C = \frac{2P\Delta t}{5,925}$$

It should be noted that the series combination (C1, C2, See Figure 9) requires each capacitor to be twice the calculated value, but the required voltage rating of each capacitor is reduced to 200V. Allowable ripple voltage on the bus (or ripple current in the capacitors) may define the capacitance requirement. Consideration should be given to converter ripple rejection and resulting output ripple voltage.

For example, a converter whose output is 15V and nominal input is 300V will provide approximately 56dB ripple rejection, i.e., 10Vp-p of input ripple will produce 15mVp-p of output ripple (See Figure 17). Equation 3 is again used to determine the required capacitance. In this case, V1 and V2 are the instantaneous values of bus voltage at the peaks and valleys (See Figure 13) of the ripple, respectively. The capacitors must hold-up the bus voltage for the time interval ($\Delta t$) between peaks of the rectified line as given by:

$$\Delta t = \frac{\pi}{2f}$$  

Where:  
\[ f = \text{line frequency} \]  
\[ \theta = \text{rectifier conduction angle} \]

The approximate conduction angle is given by:

$$\theta = \cos^{-1}\frac{V_2}{V_1}$$  

$$\theta = \cos\frac{V_2}{V_1}$$

Another consideration in hold-up capacitor selection is their ripple current rating. The capacitors’ rating must be higher than the maximum operating ripple current. The approximate operating ripple current (rms) is given by:

$$I_{\text{rms}} = \frac{2P}{V_{\text{AC}}}$$  

Where:  
\[ P = \text{operating power level} \]  
\[ V_{\text{AC}} = \text{operating line voltage} \]

Calculated values of bus capacitance for various hold-up time, ride-through time, and ripple voltage requirements are given as a function of operating power level in Figures 14, 15, and 16, respectively.

Example

In this example, the output required from the DC-DC converter at the point of load is 12V$_{\text{DC}}$ at 320W. Therefore, the output power from the FARM would be 375W (assuming a converter efficiency of 85%). The desired hold-up time is 9ms over an input range of 90 to 264V$_{\text{AC}}$.

Determining Required Capacitance for Power Fail Warning: Figure 14 is used to determine capacitance for a given power fail warning time and power level, and shows that the total bus capacitance must be at least 820µF. Since two capacitors are used in series, each capacitor must be at least 1,640µF. Note that warning time is not dependent on line voltage. A hold-up capacitor calculator is available on the Vicor website, at: vicorpower.com/hubcalc.

Determining Ride-through Time: Figure 15 illustrates ride-through time as a function of line voltage and output power, and shows that at a nominal line of 90V$_{\text{AC}}$, ride-through would be 68ms. Ride-through time is a function of line voltage.

Determining Ripple Voltage on the Hold-up Capacitors: Figure 16 is used to determine ripple voltage as a function of operating power and bus capacitance, and shows that the ripple voltage across the hold-up capacitors will be 12Vp-p.

Determining the Ripple on the Output of the DC-DC Converter: Figure 17 is used to determine the approximate ripple rejection of the DC-DC converter and indicates a ripple rejection of approximately 60dB for a 12V output. Since the ripple on the bus voltage is 12V$_{\text{AC}}$ and the ripple rejection of the converter is 60dB, the output ripple of the converter due to ripple on its input (primarily 120Hz) will be 12mVp-p.

For more information about designing an autoranging AC input power supply using the FARM and Vicor DC-DC converter modules, contact Vicor Applications Engineering at the nearest Vicor Technical Support Center, or send email to: apps@vicorpower.com.

Storage

Vicor products, when not installed in customer units, should be stored in ESD safe packaging in accordance with ANSI/ESD S20.20, “Protection of Electrical and Electronic Parts, Assemblies and Equipment” and should be maintained in a temperature controlled factory/warehouse environment not exposed to outside elements controlled between the temperature ranges of 15°C and 38°C. Humidity shall not be condensing, no minimum humidity when stored in an ESD compliant package.
Application Note (Cont.)

Figure 13 — Hold-up time

Figure 14 — Power fail warning time vs. operating power and total bus capacitance, series combination of C1, C2 (See Figure 9)

Figure 15 — Ride-through time vs. operating power

Figure 16 — Ripple voltage vs. operating power and bus capacitance, series combination of C1, C2 (See Figure 9)

Figure 17 — Converter ripple rejection vs. output voltage (typical)
PCB Mounting Specifications

- **Converter Pins No. Function Label**
  - 1 Neutral N
  - 2 EMI GND
  - 3 NC
  - 4 Line L
  - 5 -OUT -
  - 6 Enable EN
  - 7 Strap ST
  - 8 BUS OK BOK
  - 9 +OUT +

- **Module Outline**

- **PCB Mounting Specifications**
  - **NOTES:**
    - **1.** MATERIAL:
      - BASE: 6063 SERIES ALUMINUM
      - COVER: LCP, ALUMINUM 3003 H14
      - PIN: RoHS Pins Gold Plate 30 Micro Inch Min; Non-RoHS Pins: Tin-Lead 95/5 Bright
    - **2.** DIMENSIONS AND VALUES IN BRACKETS ARE METRIC
    - **3.** MANUFACTURING CONTROL IS IN PLACE TO ENSURE THAT THE SPACING BETWEEN THE MODULES LABEL SURFACE TO THE PRINTED CIRCUIT BOARD IS 0.100 BEFORE MOUNTING.
    - **4.** ALL MARKINGS ON THIS SURFACE
    - **5.** MODEMATE: GOLD PLATED COPPER
      - RoHS: GOLD PLATED COPPER
    - **6.** Consult Vicor Application Note: Soldering methods and procedures for 1st and 2nd Generation modules.

- **Converter Pins No. Function Label**
  - **FULL R (OX)**
    - Slotted
      - (OX) 0.13
    - Threaded
      - 4-40 UNC-2B (OX)
    - Thru Hole
      - #30 Drill Thru (OX) (0.128)

- **Conversion Between Pin Styles**
  - **Style 1 baseplate only**
  - **Style 2 & 3 baseplates**
  - **Extra Long Pin**
  - **Reserved for Vicor accessories**
  - **Not for mounting**
  - **Pin center line**

- **Material:**
  - BASE: 6063 SERIES ALUMINUM
  - COVER: LCP, ALUMINUM 3003 H14
  - PIN: RoHS Pins Gold Plate 30 Micro Inch Min; Non-RoHS Pins: Tin-Lead 95/5 Bright

- **Dimensions and Values:**
  - **MAXIMUM GAP AS CALCULATED FROM THE TOLERANCE STACK-UP AND IS NOT SUBJECT TO NEGATIVE TOLERANCE ACCUMULATION**

- **Dimensions and Values in Brackets**
  - **METRIC**
  - **INCHES**

- **Module Outline**
  - **PCB THICKNESS**
    - 0.062 ± 0.010
    - 1.790**
    - 0.400**
    - 0.195

  - **PLATED THROUGH HOLE DIA**
    - 0.158
    - 4.01

  - **PIN SIZES:**
    - **SHORT PIN STYLE**
      - 0.094 ± 0.003
    - **LONG PIN STYLE**
      - 0.164 ± 0.003

  - **BASEPLATE**
    - **POLYIMIDE**
    - **ALUMINUM**
    - **COVER:** LCP, ALUMINUM 3003 H14

  - **BOLT TO:**
    - 0.57 N-m
    - 5 in-lbs

  - **CHAMFER**
    - FULL R (6X)
    - 0.130 (OX)
    - 0.120 (OX)

  - **Notes:**
    - **1.** MATERIAL:
      - BASE: 6063 SERIES ALUMINUM
      - COVER: LCP, ALUMINUM 3003 H14
      - PIN: RoHS Pins Gold Plate 30 Micro Inch Min; Non-RoHS Pins: Tin-Lead 95/5 Bright
    - **2.** DIMENSIONS AND VALUES IN BRACKETS ARE METRIC
    - **3.** MANUFACTURING CONTROL IS IN PLACE TO ENSURE THAT THE SPACING BETWEEN THE MODULES LABEL SURFACE TO THE PRINTED CIRCUIT BOARD IS 0.100 BEFORE MOUNTING.
    - **4.** ALL MARKINGS ON THIS SURFACE
    - **5.** MODEMATE: GOLD PLATED COPPER
      - RoHS: GOLD PLATED COPPER
    - **6.** Consult Vicor Application Note: Soldering methods and procedures for 1st and 2nd Generation modules.
Vicor’s comprehensive line of power solutions includes high density AC-DC and DC-DC modules and accessory components, fully configurable AC-DC and DC-DC power supplies, and complete custom power systems.

Information furnished by Vicor is believed to be accurate and reliable. However, no responsibility is assumed by Vicor for its use. Vicor makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication. Vicor reserves the right to make changes to any products, specifications, and product descriptions at any time without notice. Information published by Vicor has been checked and is believed to be accurate at the time it was printed; however, Vicor assumes no responsibility for inaccuracies. Testing and other quality controls are used to the extent Vicor deems necessary to support Vicor’s product warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

Specifications are subject to change without notice.


**Vicor’s Standard Terms and Conditions and Product Warranty**

All sales are subject to Vicor’s Standard Terms and Conditions of Sale, and Product Warranty which are available on Vicor’s webpage (http://www.vicorpower.com/termsconditions/warranty) or upon request.

**Life Support Policy**

VICOR’S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS PRIOR WRITTEN APPROVAL OF THE CHIEF EXECUTIVE OFFICER AND GENERAL COUNSEL OF VICOR CORPORATION. As used herein, life support devices or systems are devices which (a) are intended for surgical implant into the body, or (b) support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in a significant injury to the user. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system or to affect its safety or effectiveness. Per Vicor Terms and Conditions of Sale, the user of Vicor products and components in life support applications assumes all risks of such use and indemnifies Vicor against all liability and damages.

**Intellectual Property Notice**

Vicor and its subsidiaries own Intellectual Property (including issued U.S. and Foreign Patents and pending patent applications) relating to the products described in this data sheet. No license, whether express, implied, or arising by estoppel or otherwise, to any intellectual property rights is granted by this document. Interested parties should contact Vicor’s Intellectual Property Department.

---

Vicor Corporation
25 Frontage Road
Andover, MA, USA 01810
Tel: 800-735-6200
Fax: 978-475-6715

email
Customer Service: custserv@vicorpower.com
Technical Support: apps@vicorpower.com