

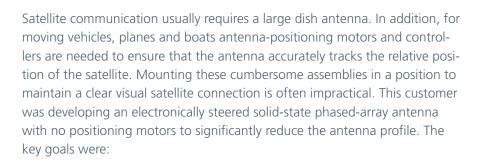
Case study: Satellite communications antenna



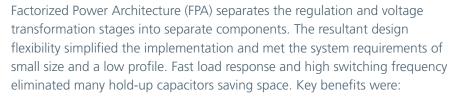
Delivering high current at very low voltage



Customer's challenge



- The antenna's processing signals required a well-regulated 1.5V 80A supply
- Provide a small, robust and reliable low profile power solution
- Accommodate future ASIC designs requiring lower core voltage and higher currents



- Performance maximized by fast response of FPA to ASIC's pulsed current demands
- Reduced footprint (11cm²) and ultra-low profile (<7mm)
- Future ASICs with lower voltages (<1V) and higher currents (+100A) can use the same, scalable, small-footprint architecture



The Vicor solution

Factorized Power Architecture provides high current to the ASIC in the lowest profile

The Power Delivery Network: The Vicor VTM (transformation) supplied the high current 1.5V rail and was placed close to the ASIC. The PRM (regulation) was sited at the PCB edge since its 48V output bus to the VTM carried only low currents and provided reduced bus distribution losses. This arrangement saved space and optimized voltage regulation at the ASIC. To analyze this power chain, go to **Vicor Whiteboard** online tool.





PRM buck-boost regulators

Non-isolated regulated

Input: 48V (36 – 75V)

Output: 48V (5 – 55V)

Power: Up to 600W

Peak efficiency: 98%

As small as

22.0 x 16.5 x 6.73mm

vicorpower.com/prm



VTM current multipliers

Isolated fixed-ratio

Input: 0 - 60V

Output: 0 – 55V

Current: Up to 115A

Peak efficiency: 97%

As small as

22.83 x 8.52 x 4.9mm

vicorpower.com/vtm

